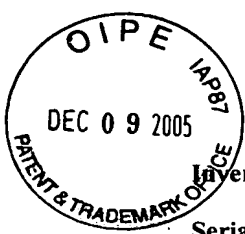


IPW



PATENT APPLICATION
Do. No. 5038-298
Intel Ref. No. P15521

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Robert M. Ellis
Serial No.: 10/713,563 Examiner: Not Yet Assigned
Filing Date: November 13, 2003 Group Art Unit: Not Yet Assigned
Title: METHOD AND APPARATUS FOR MAINTAINING DATA DENSITY FOR
DERIVED CLOCKING
Confirmation No. 8266

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application;
or before mailing date of first office action on the merits; or before the mailing of a first Office
Action after the filing of a request for continued examination under §1.114; whichever occurs
last)
- ☐ under 37 CFR 1.97 (c) together with either a:
 - ☐ Statement under 37 CFR 1.97 (e), or
 - ☐ a \$180 fee under 37 CFR 1.17 (p); or(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
- ☐ under 37 CFR 1.97 (d) together with:
 - ☐ Statement under 37 CFR 1.97 (e), and
 - ☐ a \$180.00 fee set forth in 37 CFR 1.17 (p).(Filed after final action or notice of allowance, whichever occurs first, but before payment of

the issue fee)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with
copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s)
may be material to the examination of this application and for which there may be a duty to disclose in accordance
with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A
duplicate copy of this sheet is enclosed.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Todd J. Iverson
Reg. No. 53,057

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**PATENT APPLICATION**

Atty. Docket No.: 5038-298
Applicant: Robert M. Ellis
Filing Date: November 13, 2003

Serial No. 10/713,563

Group: Not Yet Assigned

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)****U.S. PATENT DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
_____	_____	5,512,860	4-30-96	Huscroft et al.	331/1	331/11; /14; 331/25; 331/DIG.2
_____	_____					
_____	_____					
_____	_____					

FOREIGN PATENT DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	WO 85/00259	1-17-85	DE, DE (European patent), FR (European patent) GB, GB (European patent) JP	M/A-COM DCC, Inc..
_____	_____	WO04/102403 A2	11-25-04	All designated states	Advanced Micro Devices, Inc.
_____	_____				
_____	_____				

OTHER DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	"IEEE std 1596.4-1996 – IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)" IEEE STD 1596.4-1995, XX, XX 31 December 1996 (1996-12-31), pages 1-91, XP002333770, figure 14, page 44

Examiner: _____

Date Considered: _____